

## IN THE SPECIFICATION

Please replace the paragraph beginning at page 4, line 5 with the following:

The ingress memory hub 18 sends ingress flow id information, such as an ingress flow Id, packet size, etc. to the ingress traffic manager 16. The ingress traffic manager 16 sends scheduling commands back to the ingress memory hub 18 indicating when packets for different ingress flow Ids are output to the switch fabric 22. In one example, the ingress traffic manager 16 is located on a separate integrated circuit or circuit board than the ingress memory hub 18. However, the ingress traffic manager and the ingress memory hub could also be part of the same circuit. This also applies for the egress memory hub and egress traffic manager. When the memory hub and traffic manager are part of the same circuit, the communication between them may or may not be the same as ~~describe~~ described below.

Please replace the paragraph beginning at page 13, line 18 with the following:

The ingress traffic manager 16 updates ingress queues #1 and #2. The ingress queues #1 and #2 track the total packets, total length, and individual packet lengths for ingress flows Ids #1 and #2. Ingress queue #1 indicates two total packets and a total length for ingress flow Id #1 as 3L. An ordered queue for ingress flow Id #1 identifies the order and size of the received packets A and C as L and 2L, respectively. Ingress queue #2 in ingress traffic manager 16 identifies the total number of received packets as 1 and the total length of ingress flow Id #2 as L. The ordered queue for ingress queue #2 has only one entry identifying packet ~~C~~ B as length L.